

REMARKS

Claims 1-33 and 35-43 are presently pending. Claims 17, 22, and 26 are amended. Claim 34 is cancelled without prejudice. Claims 1-36 and 38-41 were rejected. Claims 37, 42, and 43 were objected to but indicated as allowable. Assignee appreciates Examiner's indication of allowable subject matter.

Examiner has objected to the drawings and specification. In view of the amendments made to the drawings and specification, it is respectfully submitted that the objections are overcome.

Claims 17, 22, and 26 were rejected under 35 U.S.C. § 112, second paragraph. Claims 17, 22, and 26 are amended, and it is respectfully submitted that these claims as amended overcome these rejections.

Claims 1, 8, 16, and 21 were rejected 35 U.S.C. § 103(a) as being obvious from Mino in view of Childers. Examiner has indicated that "Mino et al. fails to disclose converting (a second circuit for converting) the byte order of the plurality of pixels to a predetermined order ("standard format"), the byte order being different from the predetermined byte/pixel order (Fig. 4B, "byte reordering logic 457", Col. 6 lines 35-40). ... it would have been obvious to one of ordinary skill in the art at the time the invention was made to modify Mino et al.'s method/system by incorporating the method step and the byte reordering logic for converting the byte order of the plurality of pixels to a predetermined byte order, the byte order being different from the predetermined byte order in order to write and read pixel data in a standard format regardless of the format in which a video input device creates it (Col. 6, Lines 35-40)."

"If proposed modification would render the prior art invention being modified unsatisfactory for its intended purpose, then there is no suggestion or motivation to make the proposed modification. *In re Gordon*, 733 F.2d 900, 221 USPQ 1125 (Fed. Cir. 1984)". MPEP 2143.01. Assignee respectfully submits that the modification proposed by Examiner of Mino would render Mino in operable, and therefore unsatisfactory for its intended purpose.

Mino does not teach use of "a predetermined byte order, the byte order being different from the predetermined byte order". However, a "data transfer between the host CPU 1 and the general region 21 of the local memory 20" occurs. See, Col. 6, Lines 3-5. If Mino et al.'s method/system was modified "by incorporating the method step and the byte reordering logic for converting the byte order of the plurality of pixels to a predetermined byte order, the byte order being different from the predetermined byte order", the CPU would be incompatible, and thus unsatisfactory for its intended purpose.

Secondly, Mino also teaches away from Examiner's proposed combination. For example, Mino teaches that "Therefore, the consistency of the format of data transferred can be maintained even when no sequentiality is kept between the timing of data transfer between the system bus and the general region and the timing of data transfer between the system bus and the frame buffer region." Mino, Col. 2, Lines 52-57 (Emphasis Added). Clearly, if it is desirable that "the consistency of the format of data transferred can be maintained", one skilled in the art would not be motivated to modify Mino with Childers as proposed by Examiner.

Accordingly, for at least each of the foregoing reasons, Assignee respectfully traverses the rejection and requests that Examiner withdraw the rejection to claims 1, 8, 16, and 21 and dependent claims 2-7, 9-25, 17-20, and 22-26.

With regards to claim 27 and 38, Examiner has indicated that "it would have been obvious to one of ordinary skill in the art at the time the invention was made to modify Mino et al's method/system by incorporating the method steps and registers for storing luma pixels in a luma pixel register, if the plurality of pixels are luma pixels; and storing chroma pixels in a chroma pixel register, if the plurality of pixels comprise chroma pixels in order to require a simple hardware configuration to generate video signals such as video control information (col. 3, lines 47-54)."

Mino also teaches away from Examiner's proposed combination. For example, Mino teaches that "Therefore, the consistency of the format of data transferred can be maintained even when no sequentiality is kept between the timing of data transfer between the system bus and the general region and the timing of data transfer between the system bus and the frame buffer region." Mino, Col. 2, Lines 52-57 (Emphasis Added). Clearly, if it is desirable that "the consistency of the format of data transferred can be maintained", one skilled in the art would not be motivated to modify Mino with Baker as proposed by Examiner, because "incorporating the method steps and registers for storing luma pixels in a luma pixel register, if the plurality of pixels are luma pixels; and storing chroma pixels in a chroma pixel register, if the plurality

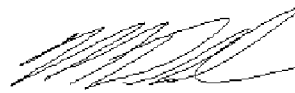
of pixels comprise chroma pixels" would not maintain consistency of the format of data transferred.

Accordingly, for at least the foregoing reasons, Assignee respectfully traverses the rejection and requests that Examiner withdraw the rejection to claims 27 and 38, as well as to and dependent claims 28-33, 35, 36, and 39-41.

Conclusion

For the foregoing reasons, Assignee respectfully submits all of the pending claims are in a condition for allowance, thereby placing the application in a condition for allowance. It is believed that there is no fee associated with any of the actions requested herein. To the extent that there is any fee associated with any actions requested herein, the Commissioner is requested to charge such fee to Deposit Account 13-0017.

RESPECTFULLY SUBMITTED



Mirut Dalal - Reg. No. 44,052  
ATTORNEY FOR ASSIGNEE

June 21, 2007

McAndrews, Held & Malloy, Ltd.  
500 West Madison - Suite 3400  
Chicago, IL 60661

Phone (312) 775-8000  
FAX (312) 775-8100